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Virtual Synchronous Machine Grid-Forming Inverter Model (REGFM\_B1)

**U.S. DEPARTMENT OF** 

ENERGY

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#### **VSM GFM Model**

- A virtual synchronous machine (VSM) grid-forming inverter (GFM) model specification has been developed through the collaboration between PNNL, EPRI, and GE
- The steady state current limiting and fault ride-through control blocks came from a GE patent <sup>[1]</sup>
- SGRE also provided inputs to the model specification
- User-defined PSS/E and PSCAD models were developed at PNNL based on the model specification, and simulation results were compared



#### Virtual Synchronous Machine Grid-Forming Inverter Model Specification (REGFM\_B1)

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[1] Larsen EV, Delmerico RW, inventors; General Electric Co, assignee. Battery energy storage power conditioning system. United States patent US 5,798,633. 1998 Aug 25.

#### **VSM GFM Model**

- The VSM GFM model specification includes the VSM/inertial control block and the voltage control block
- A PLL is used to obtain the angle of the terminal voltage, and the VSM/inertial block controls the angle difference  $\delta_{IT}$  ( $\omega$ Flagb=0)
- The use of PLL in the VSM GFM is different from its use in GFLs
- The PLL can be considered as not existed during normal operation







Voltage source behind impedance



VSM/inertial control block

### **Steady State Current Limiting of VSM GFM**

- The VSM GFM model specification includes the steady state active and reactive current limiting controls
- The active current  $I_d$  can be limited by regulating the saturation limits of  $\delta_{IT}$  of the VSM/inertial control block









Active current I<sub>d</sub> limiting

VSM/inertial control block

#### **Steady State Current Limiting of VSM GFM**

- The reactive current  $I_q$  can be limited by regulating the saturation limits  $E_{max}$  and  $E_{min}$  of the voltage control block
- The steady state current limiting control adopts a P-priority approach



Voltage source behind impedance





#### **Transient Current Limiting of VSM GFM**

 A transient current limiting function is used to limit the output current algebraically at the beginning of a fault, which is the same as used by REGFM\_A1





### Testing in a Single-GFM Infinite-Bus System

- Both PSS/E and PSCAD models were developed based on the VSM GFM model specification
- All parameters were set the same in the PSS/E and PSCAD models
- The PSCAD model is an IGBT-based detailed model that includes additional PWM control, and the transient current limiting is implemented at the PWM control layer
- The transient current limiting in PSS/E is implemented algebraically
- 18 variables were compared

## Islanding

After disconnecting from the grid, the two GFMs can maintain the stable operation of the grid





Source: NERC GFM report

Grid Forming Functional Specifications for BPS-Connected Battery Energy Storage Systems

June 2023

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### Islanding (PSS/E and PSCAD comparison)

• 18 variables were compared and achieved a good level of alignment





#### 5% Voltage Drop

- When the grid voltage drops, the VSM GFM increases the reactive power Q
- The steady state Q follows the Q-V droop





#### 5% Voltage Drop (PSS/E and PSCAD comparison)

• 18 variables were compared and achieved a good level of alignment





#### Frequency Drop (59.5 Hz)

- The VSM GFM responds to the frequency change by increasing *P* autonomously
- The frequency drop results in the overload of VSM GFM
- I<sub>d</sub> was limited by limiting the angle  $\delta_{IT}$









Active current  $I_d$  limiting



#### Frequency Drop (PSS/E and PSCAD comparison)

• 18 variables were compared and achieved a good level of alignment





#### 0.1 s fault

- The transient current limiting clips the current at  $I_{maxF}$  1.5 pu
- The reactive current limiting ( $E_{max}$  and  $E_{min}$ ) later limits the current at 1 pu
- The active current limiting also plays a role in limiting the steady-state current





#### 0.1 s fault (PSS/E and PSCAD comparison)

• 18 variables were compared and achieved a good level of alignment



#### 1 s fault

- The transient current limiting clips the current at  $I_{maxF}$  = 1.5 pu
- The steady-state current limiting later limits the current at  $I_{max}$  = 1 pu by reducing *E* and clamping phase angle
- Initial overcurrent post fault because of reduced E





### 1 s fault (PSS/E and PSCAD comparison)

• 18 variables were compared and achieved a good level of alignment





#### ERCOT LVRT Test <sup>[1]</sup>

• 18 variables were compared and achieved a good level of alignment



[1] Yunzhi Cheng, https://sites.google.com/view/dmview/home



#### Conclusions

- A VSM GFM model specification has been developed through the collaboration between PNNL, EPRI, and GE
- The VSM GFM model includes the VSM/inertial control block, Q-V droop control block, transient current limiting function, and steady state active and reactive current limiting control blocks
- User-defined PSS/E and PSCAD models were developed and tested under various disturbances in a single-GFM infinite-bus system, and the PSS/E and PSCAD results match well



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# **THANK YOU**

#### 50% Voltage Drop







#### **50% Voltage Drop**



- PSCAD

- PSCAD

- PSCAD

- PSCAD

PSCAD

– – – PSSE

4.5

— — PSSE

4.5

– – – PSSE

4.5

— — PSSE

4.5

5

5

5

5

5

– – – PSSE

4.5



